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Attorney's Docket No. 10559-310US1 / P9631
DT05 Recd PCT/PTO 28 OCT 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gilbert Wolrich et al.
Serial No. : 10/070,091
Filed : February 27, 2002
Title : REGISTER SET USED IN MULTITHREADED PARALLEL PROCESSOR
ARCHITECTURE

Commissioner for Patents
Washington, D.C. 20231

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REQUEST FOR CORRECTED OFFICIAL FILING RECEIPT Technology Center 2100

Please correct the Filing Receipt for the above-referenced application to include the correct Priority data as follows:

"THIS APPLICATION IS A 371 OF PCT/US00/23993 08/31/2000

WHICH CLAIMS BENEFIT OF 60/151,961 09/01/1999"

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Applicant: Gilbert Wolrich et al.
Serial No. : 10/070,091
Filed : February 27, 2002
Page : 2

Attorney's Docket No.: 10559-310US1 / P9631

Respectfully submitted,

Date: 10/21/02


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OCT 28 2002

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PA631

APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/070,091	06/28/2002	2183	266	10559-310US1	6	22	3

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 225 Franklin Street
 Boston, MA 02110-2804

FILING RECEIPT



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AUG 12 2002

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Date Mailed: 08/06/2002

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Applicant(s)

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Priority Date of 60/151,961 shall be 9/1/1999

DOCKETED BY PRACTICE SYSTEM	
Action Code	Cor. OER
Base Date	9-6-02
Due Date	9-6-02
Deadline	-
Initials	PEC On

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Technology Center 2100

Domestic Priority data as claimed by applicant

THIS APPLICATION IS A 371 OF PCT/US00/23993 08/31/2000
 WHICH CLAIMS BENEFIT OF 60/151,961 02/22/2000

09/01/1999

Foreign Applications

Projected Publication Date: Not Applicable, filed prior to November 29,2000

Non-Publication Request: No

Early Publication Request: No

Docketed By Billing Secretary
 Due Date: _____
 Deadline: _____
 Initials: _____

Title

Register set used in multithreaded parallel processor architecture

REPORTED
TO INTEL

Preliminary Class

712

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